

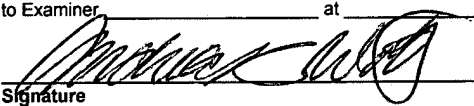


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PATENT *Image*

Confirmation No.: 5731  
Notice of Allowance Date : August 14, 2003  
Serial No.: 10/065,016

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Olivier Boireau  
For: Integrated Circuit Package and Printed Circuit Board Arrangement  
Serial No.: 10/065,016 Examiner: Lourdes C. Cruz  
Filed: September 10, 2002 Art Unit: 2827  
Confirmation No.: 5731 Docket No.: 71522-2

CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8(a))	
I hereby certify that this correspondence is, on the date shown below, being:	
<input checked="" type="checkbox"/> deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450	<input type="checkbox"/> transmitted by facsimile to the Patent and Trademark Office. to Examiner _____ at _____
Date: <u>August 18 2003</u>	 Signature <u>Andrea R. Wolters</u> (type or print name of person certifying)

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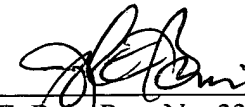
Sir:

Enclosed are 2 sheets of formal drawings for filing in the above-identified application.

Respectfully submitted:

Olivier Boireau

Dated: August 15, 2003

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